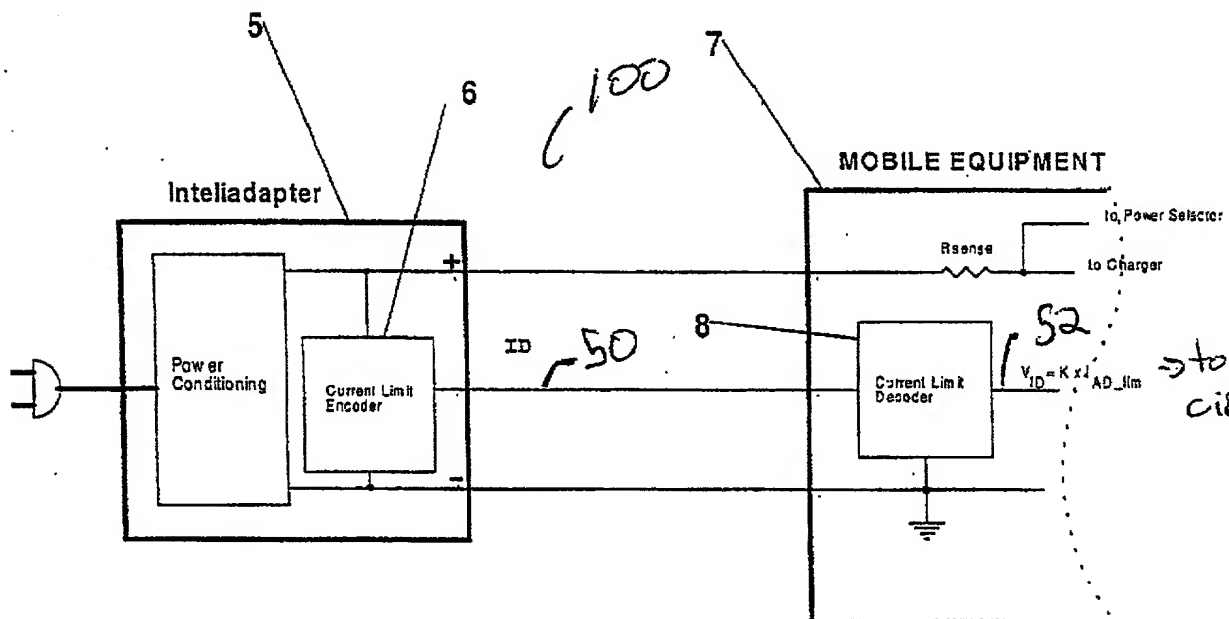


Prior Art

to power management dlc or charger circuit
Indicative of maximum adapter current
Binary



to charge circuit

Fig.2

20250101 01:00:00

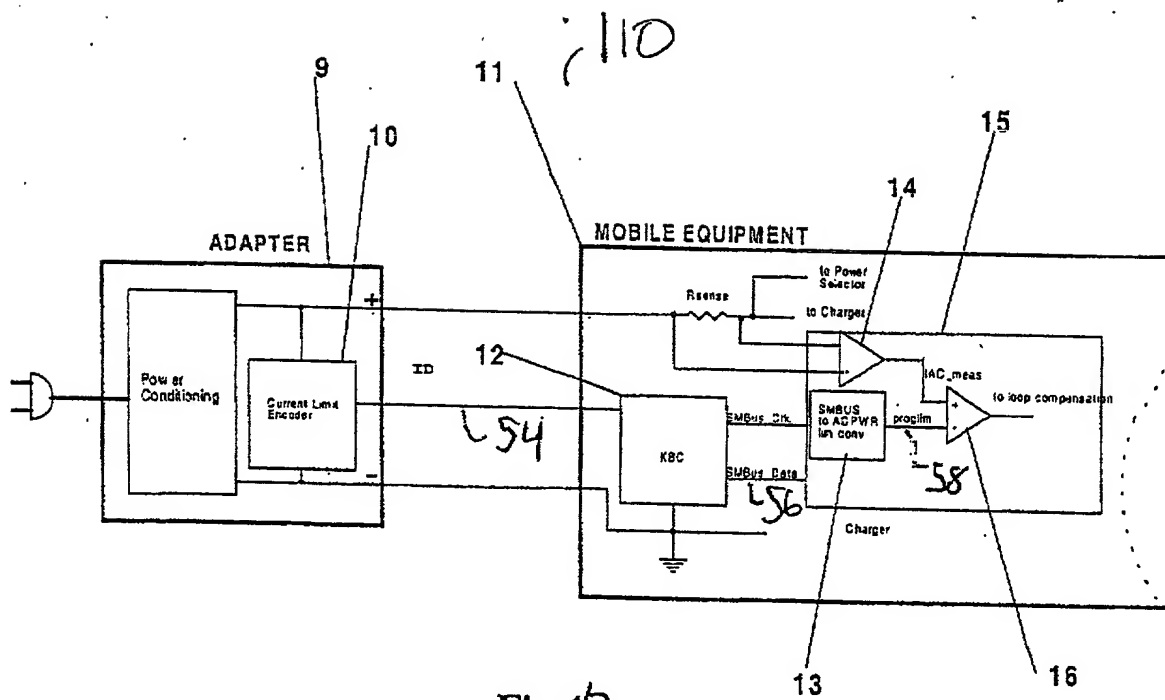


Fig. 4/3

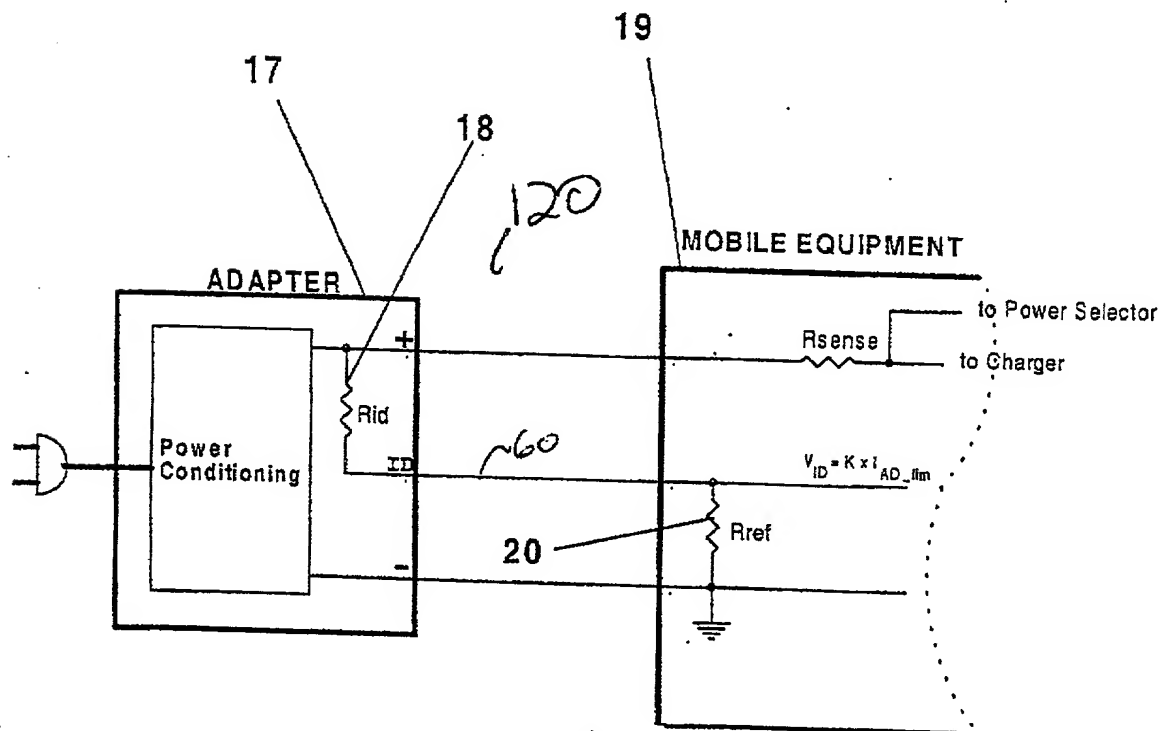


Fig. 5/4

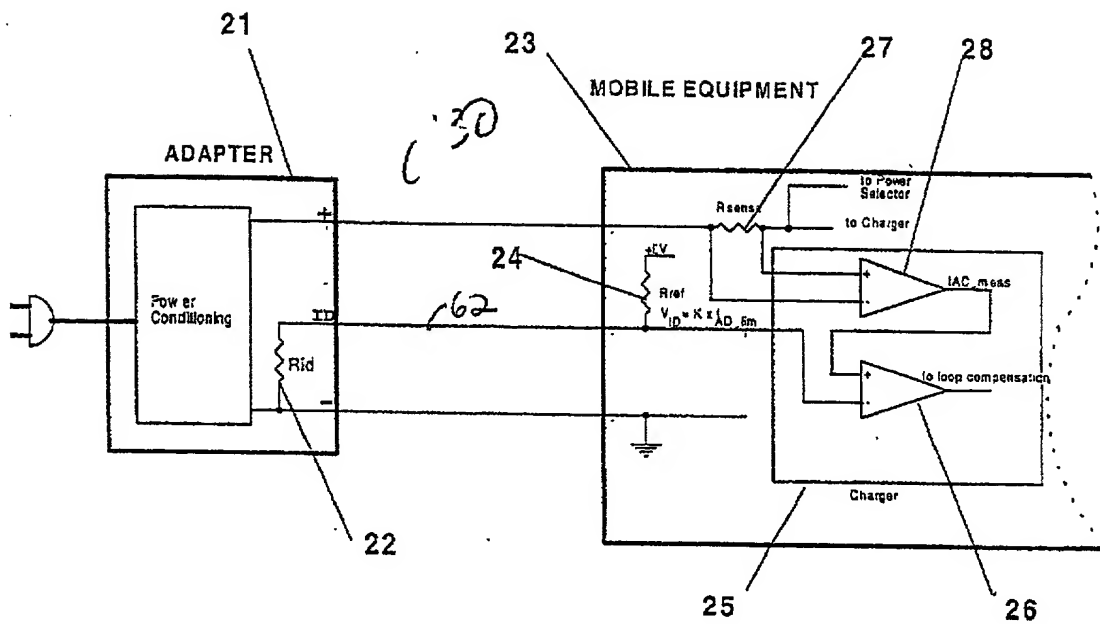


Fig. 7/5

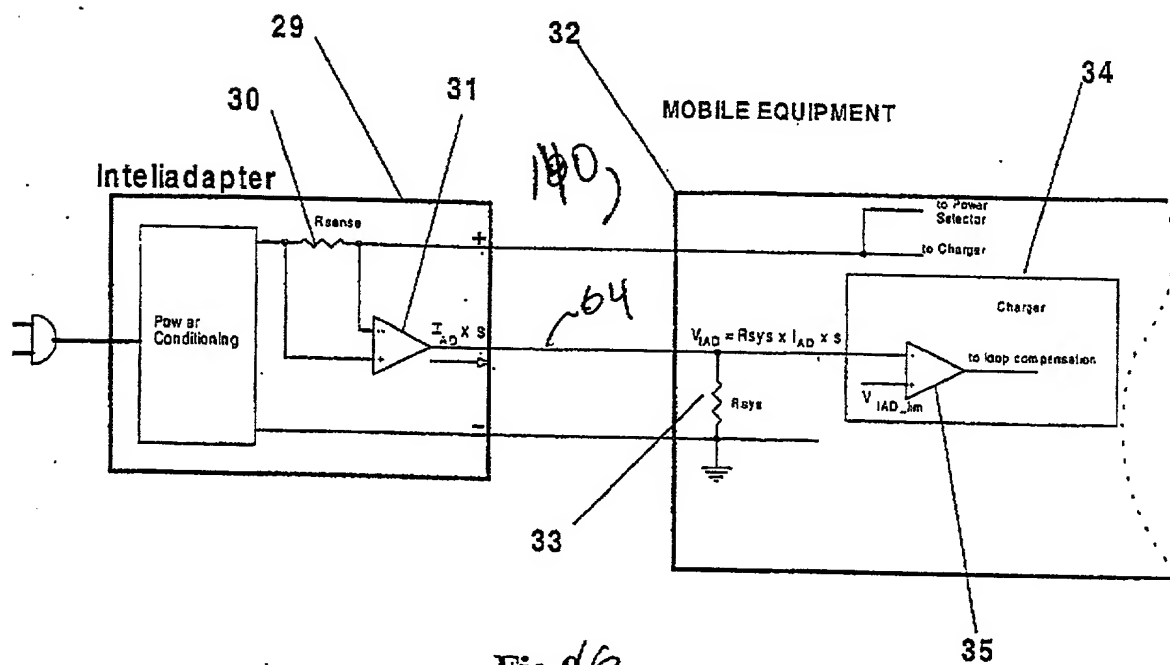


Fig. 9/6

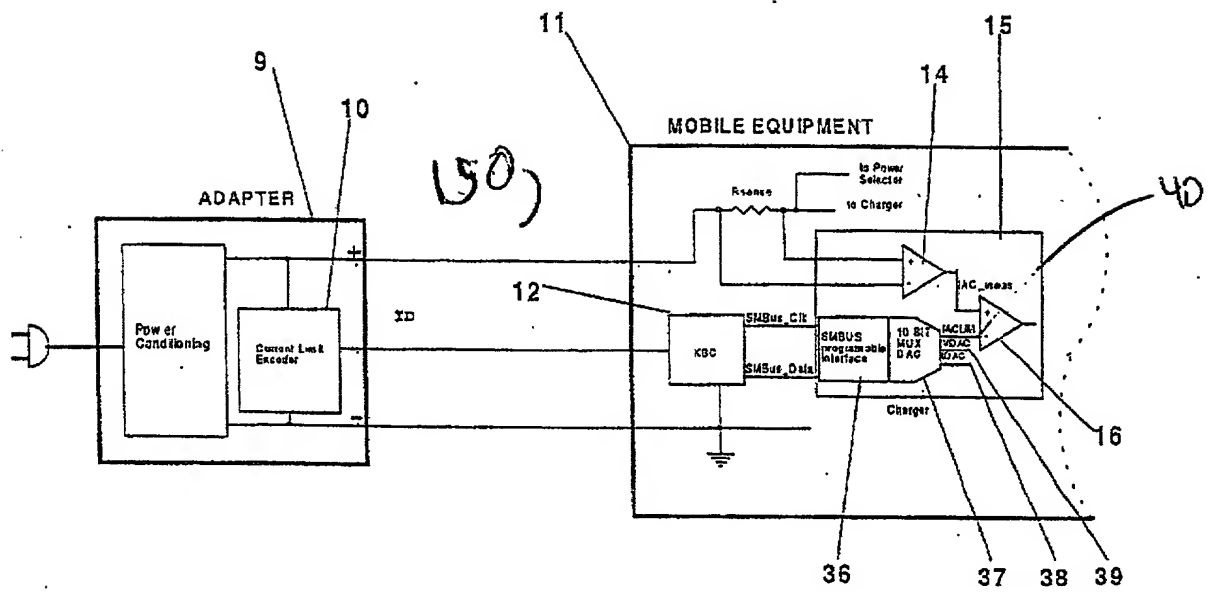


Figure 7